Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	536	701/115.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:28
L3	396	memory and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:13
L4	238	(volatile ram eeprom) and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:28
L5	526	701/114.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:28
L6	389	(volatile ram eeprom memory) and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:28
L7	389	((volatile ram eeprom eprom) wth memory)and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:31
L8	338	7 not 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:30
L9	1456	701/35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:31
L10	1155	((volatile ram eeprom eprom) wth memory)and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:33
L11	630	user and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:31

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L12	659	((volatile ram eeprom eprom) wth non adj volatile)and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:34
L13	8952	calibration adj data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:35
L14	26	12 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 08:35
S1	1	"20030225493".did.	US-PGPUB	OR	ON	2005/03/10 15:56
S2	0	wo02069627	EPO; DERWENT	OR	ON	2005/02/26 16:31
S3	0	wo02/069627	EPO; DERWENT	OR	ON	2005/02/26 16:31
S4	0	wo-02069627.did.	EPO; DERWENT	OR	ON	2005/02/26 16:32
S5	0	wo-02069627-\$.did.	EPO; DERWENT	OR	ON	2005/02/26 16:32
S6	7	"0561271"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON .	2005/02/26 16:44
S7	0	"ep0561271"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/02/26 16:44
S8	25	"561271"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/02/26 16:45
S9	1914	(711/161,162,173).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/03/10 15:56
S10	949	(701/114,115).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/03/10 15:56

S11	0	("002and3").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/03/10 15:56
S12	2	S9 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON .	2005/03/10 15:57
S13	6	("5572466" "5577194" "5809558" "5828977" "6046937" "6226728"). PN. OR ("6598114").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/10 15:58
S14	4	("6046937").URPN.	USPAT	OR	ON	2005/03/10 15:59
S15	366	711/161.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:23
S16	2057	(711/162,163).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/03/20 23:24
S17	2	("0000034").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/03/20 23:24
S18	2220	S15 S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:24
S19	94	calibration adj data with engine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:26
S20	1	S18 and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:26
S21	0	("6766425").URPN.	USPAT	OR	ON	2005/03/20 23:27
S22	9	("5394327" "5600818" "5624316" "6101600" "6263412" "6308121" "6505105" "6525952" "6591209"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/20 23:27

S23	9	("4478193" "5091858" "5265576" "5394327" "5445126" "5477827" "5483941" "6308121").PN. OR ("6505105").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/20 23:32
S24	76893	volatile adj memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:32
S25	67490	non-volatile adj memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:32
S26	67485	S24 with S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:33
S27	307	user adj changeable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:34
S28	3	S24 with S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:37
S29	526	701/114.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:37
S30	536	701/115.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:37
S31	954	S29 S30	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:38
S32	3	S18 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:39

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S33	1998	engine adj control adj module	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:42
S34	5	S24 with S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:40
S35	8307	engine adj control adj unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:42
S36	192	S24 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:51
S37	0	partioning with volatile with memory with engine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:52
S38	1	partitioning with volatile with memory with engine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:53
S39	62	partitioning with volatile with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/20 23:53
S40	0	gb0421562	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 00:33
S41	0	"0421562".ap.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 00:33
S42	6	"421562".ap.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 00:33

S43	2	"20030225493"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/03/21 00:44
S44	163	S18 and engine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR .	ON	2005/03/21 00:44
S45	0	("6804752").URPN.	USPAT	OR	ON	2005/03/21 00:50
S46	6	("4644494" "4646241" "4729102" "6026293" "6151657" "6250548"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/21 00:50



(12) United States Patent

Augsburger et al.

(10) Patent No.:

US 6,633,807 B2

(45) Date of Patent:

Oct. 14, 2003

(54) ENHANCED MODULE CHIPPING SYSTEM

(75) Inventors: Brett Augsburger, Auburn, AL (US); Eddie Burwell, Huntsville, AL (US); Frank Dudel, Huntsville, AL (US)

(73) Assignee: Audi Performance & Racing, Auburn,

AL (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 218 days.

(21) Appl. No.: 09/929,479

(22) Filed: Aug. 14, 2001

(65) Prior Publication Data

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Related U.S. Application Data

(60) Provisional application No. 60/225,196, filed on Aug. 14, 2000.

(52) U.S. Cl. 701/115; 711/100; 711/163

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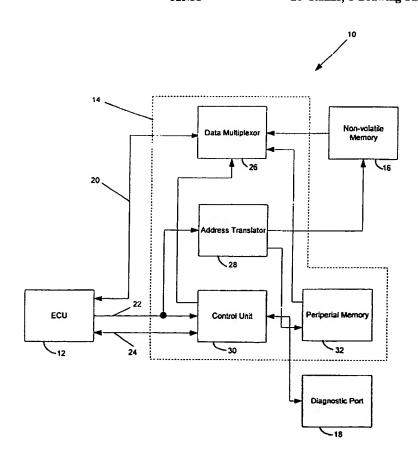
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Primary Examiner—Hieu T. Vo (74) Attorney, Agent, or Firm—Renner, Otto, Boisselle & Sklar, LLP

(57) ABSTRACT

A memory system for limiting access of a protected portion of a memory to a predetermined accessing device. The memory system includes a memory having a protected portion. The system further includes a control unit in communicative relation with an accessing device and the memory, wherein the control unit monitors the accessing device's pattern of access to the memory in order to determine if the accessing device is the predetermined accessing device and permits only the predetermined accessing device to access the protected portion of the memory.

20 Claims, 8 Drawing Sheets



3/21/05, EAST Version: 2.0.1.4



(12) United States Patent

Sommer et al.

(10) Patent No.:

US 6,516,265 B1

(45) Date of Patent:

Feb. 4, 2003

(54) METHOD AND DEVICE FOR CONTROLLING PROCESSES IN CONJUNCTION WITH AN INTERNAL COMBUSTION ENGINE

(75) Inventors: Rainer Sommer, Stuttgart (DE); Taskin Ege, Tamm (DE)

(73) Assignee: Robert Bosch GmbH, Stuttgart (DE)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 0 days.

(21) Appl. No.: 09/686,657

(22) Filed: Oct. 11, 2000

(30) Foreign Application Priority Data

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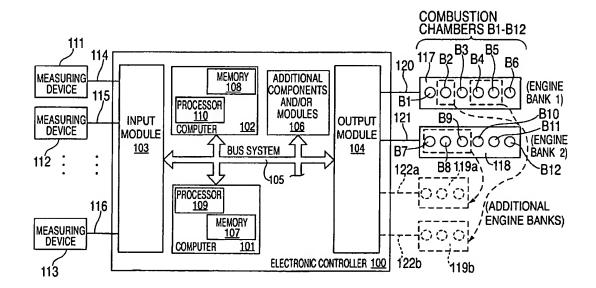
Primary Examiner—Andrew M. Dolinar

(74) Attorney, Agent, or Firm-Kenyon & Kenyon

57) ABSTRACT

A method and a device for controlling processes in conjunction with an internal combustion engine having at least two combustion chambers, where control functions are executed by at least one processor, and the program code on which the control functions are based can be stored in at least one memory. At least one data record is assigned to the program code, and the control functions are implemented as a link between the program code and the at least one data record. The combustion chambers are grouped in at least two engine banks for control purposes, and one data record is assigned to each engine bank individually. The respective data record for implementing the control functions is selected as a function of the respective engine bank to be controlled. Thus, the data record is switched as a function of the engine bank.

10 Claims, 4 Drawing Sheets



3/21/05, EAST Version: 2.0.1.4